

**VIDYA PRATISHTHAN'S COLLEGE OF ENGINEERING, BARAMATI**

Department of Electronics & Telecommunication

Departmental Time Table 2022-23

SEM-I

Day	Time	9.15 To 10.15	10.15 To 11.15	11.15 To 11.30	11.30 To 12.30	12.30 To 01.30	1.30 To 2.45	02.45 To 03.45	03.45 To 04.45
	Class/Period	1	2		3	4		5	6
Monday	S. E.	DE-A1 (DE Lab) VSS DS-A2 (LIC Lab ) PKK A4-M-III (Tut) (1 Hour) (213)			M-III-SAT (213)	EC & M-SUK (213)		DS - PKK (213)	A3-M-III (Tut) (213)
	T. E.	EM-(MMJ) (213)	MC-(JSR) (213)		MC-A1 (SP Lab) JSR DC-A2 (COMM LAB) SBL DBM-A4 (VLSI LAB) SSJ SD-A3-(LIC LAB) PKK			DBM-SSJ (203)	EM-(MMJ) TUT- A2 (203)
	B. E. (A)	EL-I-A2 (SP Lab) SSJ RMT-A1 (MW Lab) VUD CC-A3 (Digital lib. lab-1) RSP			VLSI-RKS (203)	EL-II- SDB (203)		RMT-VUD (208)	
	B. E. (B)	RMT-BHP (208)	VLSI-RKS (208)		EL-II- SDB (208)	CC-RSP (208)		VLSI-B1 (VLSI Lab) RKS EL-I-B3 (SP Lab) SDB RMT-B2 (MW Lab) BHP	
Tuesday	S. E.	DE-A3 (DE Lab) JSR ESD-A1 (AVE Lab) VSS EC & M-A2 (Elec-M/C-1) SUK DS-A4 (LIC Lab ) PKK			EC - VUD (213)	DC - VSS (203)		M-III-SAT (213)	DS - PKK (213)
	T. E.	DBM-SSJ (213)	MC-(JSR) (213)		Java-A1 (VLSI Lab) SUK MC-A2 (SP Lab) JSR DC-A3 (COMM LAB) SBL SD-A4-(LIC LAB) PKK			Java-(SUK) (203)	EM-(MMJ) TUT- A4 (203)
	B. E.(A)	CC-A1 (Digital lib. lab-1) RSP VLSI-A2 (VLSI Lab) RKS RMT-A3 (MW Lab) VUD			CC-RSP (203)	EL-I-SSJ (203)		RMT-VUD (208)	EL-II- SDB (208)
	B. E.(B)	EL-I-SDB (208)	RMT-BHP (208)		VLSI-RKS (208)	EL-I-SDB (208)		CC-B1(Digital lib. lab-1) RSP VLSI-B2 (VLSI Lab) VSS RMT-B3 (MW Lab) BHP	
Wednesday	S. E.	DS-A3 (LIC Lab ) PKK EC & M-A1(Elec-M/C-1) BHP DE-A2 (DE Lab) VSS EC-A4 (SSD LAB) VUD			DC - VSS (203)	M-III-SAT (213)		EC - VUD (213)	EC & M-SUK (213)
	T. E.	DC-(SBL) (213)	MC-(JSR) (213)		MC-A3 (SP Lab) JSR Java -A2 (VLSI Lab) SUK DC-A4 (COMM LAB) SBL DBM-A1 (LIC LAB) SSJ			EM-(MMJ) (203)	SD-A1-(LIC LAB) PKK
	B. E.(A)	VLSI-A1 (VLSI Lab) RKS EL-I-A3 (SP Lab) SSJ Project Phase-1			CC-RSP (203)	RMT-VUD (203)		EL-I-SSJ (208)	VLSI-RKS (208)
	B. E.(B)	CC-RSP (208)	EL-II- SDB (208)		VLSI-RKS (208)	EL-I-SDB (208)		VLSI-B3 (VLSI Lab) VSS EL-I-B1 (SP Lab) SDB CC-B2 (Digital lib. lab-1) RSP	
Thursday	S. E.	DS-A1 (LIC Lab ) PKK EC-A2 (SSD Lab) VUD ESD-A3 (AVE Lab) VSS DE-A4 (DE Lab) JSR			DC - VSS (213)	EC - VUD (213)		DS - PKK (213)	EC & M-SUK (213)
	T. E.	EM-(MMJ) TUT-A3 (213)	Java-(SUK) (213)		MC-A4 (SP Lab) JSR Java-A3 (VLSI Lab) SUK DBM-A2 (LIC LAB) SSJ			EM-(MMJ) (203)	DC-(SBL) (203)
	B. E.(A)	EL-I-A1 (SP Lab) SDB CC-A2 (Digital lib. lab-1) RSP VLSI-A3 (VLSI Lab) RKS			EL-II- SDB (203)	CC-RSP (203)		VLSI-RKS (208)	EL-I-SSJ (208)
	B. E.(B)		RMT-BHP (208)		CC-RSP (208)	EL-II- SDB (208)		EL-I-B2 (SP Lab) SDB RMT-B1 (MW Lab) BHP CC-B3 (Digital lib. lab-1) RSP	

SHORT RECESS

Lunch Break

Friday	S. E.	EC-A1 (SSD Lab) VUD A2- M-III (Tut) (213) 1 Hour EC & M-A3 (Elec-M/C-1) SUK ESD-A4 (AVE Lab) VSS		M-III-SAT (213)	Java-A4 (SP Lab) SUK DC-A1 (COMM LAB) SBL DBM-A3 (VLSI LAB) SSJ SD-A2-(LIC Lab) PKK	A1-M-III (Tut)(1 Hour) EC & M-A4 (Elec-M/C-1) BHP EC-A3 (SSD LAB) VUD ESD-A2 (AVE Lab) VSS	
	T. E.	DC-(SBL) (213)	DBM-SSJ (213)			Java-(SUK) (203)	EM-(MMJ) TUT-A1 (203)
	B. E.(A)	RMT-A2 (MW Lab) BHP Project Phase-1				Project Phase-1	
	B. E.(B)	Project Phase-1				Project Phase-1	
Saturday	S. E.						
	T. E.						
	B. E.						

*V.S. Surwase*  
Time-Table In-Charge  
V.S. Surwase

*M.D. Shelar*  
Time-Table Co-ordinator  
M.D. Shelar

*Dr. B.H. Patil*  
29-7-22.  
H.O.D.  
Dr.B.H.Patil

*Dr. R.S. Bichkar*  
Principal  
Dr.R.S.Bichkar  
Principal

Vidya Pratishthan's  
Kamalnayan Bajaj Institute of  
Engineering & Technology, Baramati  
Vidyanagari, Baramati-413133

SEA  
M-III:-Engineering Mathematics III-  
EC:-Electronic Circuits :- V.U.Deshmukh  
DC:- Digital Circuits :- V.S. Surwase  
EC :-Electrical Circuits:- S.U. Kolhar  
DS :Data structures :- P.K.Kadbe  
ESD:Electronic Skill Development:- V.S. Surwase

TE A  
MC:- Microcontrollers :- Dr. J.S.Rangole  
DC:- Digital Communication :- Dr. S.B. Lande  
DBM:- Data Base Management:-S.S. Jagdale  
EM:-Electromagnetics :- M.M.Jadhav  
SD: skill development: P.K.Kadbe  
EL-I(Java):-S.U.Kolhar

BE A  
VLSI : VLSI Design : Dr.R.K. Shastri  
CC :Cloud computing: R.S.Piske  
RMT:- Radiation & Microwave Techniques :- V.U.Deshmukh  
EL-I: :- S.S. Jagdale  
EL-II :- (EPD) :- S.D.Biradar

BE B

VLSI : VLSI Design : Dr.R.K. Shastri/ V.S. surwase  
CC :Cloud computing: R.S.Piske  
RMT:- Radiation & Microwave Techniques :- Dr. B.H.Patil  
EL-I: :- S.D. Biradar  
EL-II :- (EPD) :- S.D.Biradar